

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Michael B. Galles et al.	Confirmation No.:	5506
Serial No.:	10/696,146	Art Unit	2181
Filed:	October 29, 2003	Examiner:	Treat, William M.
For:	Multi-Processor System and Method of Accessing Data Therein	Attorney Docket No.:	001361-5159-US

STATEMENT UNDER 37 C.F.R. § 3.73(b)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

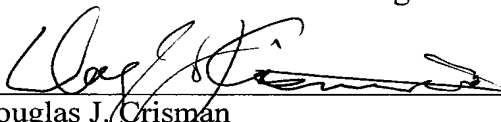
Sir:

Silicon Graphics International Corp., a corporation, states that it is the assignee of the entire right, title and interest in the parent of the patent application identified above by virtue of an assignment from the inventor(s) in the parent of the patent application/patent identified above:

1. From: Michael B. Galles and Jeffrey S. Kuskin  
To: Silicon Graphics, Inc.  
Recorded: October 15, 1999 Reel: 010325 Frame: 0832
2. From: Silicon Graphics, Inc. et al. and SGI International, Inc.  
To: Silicon Graphics International Corp.  
Recorded: February 16, 2012 Reel: 027727 Frame: 0020

The undersigned is authorized to act on behalf of the assignee.

Date: July 24, 2012

  
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